Fig. 1

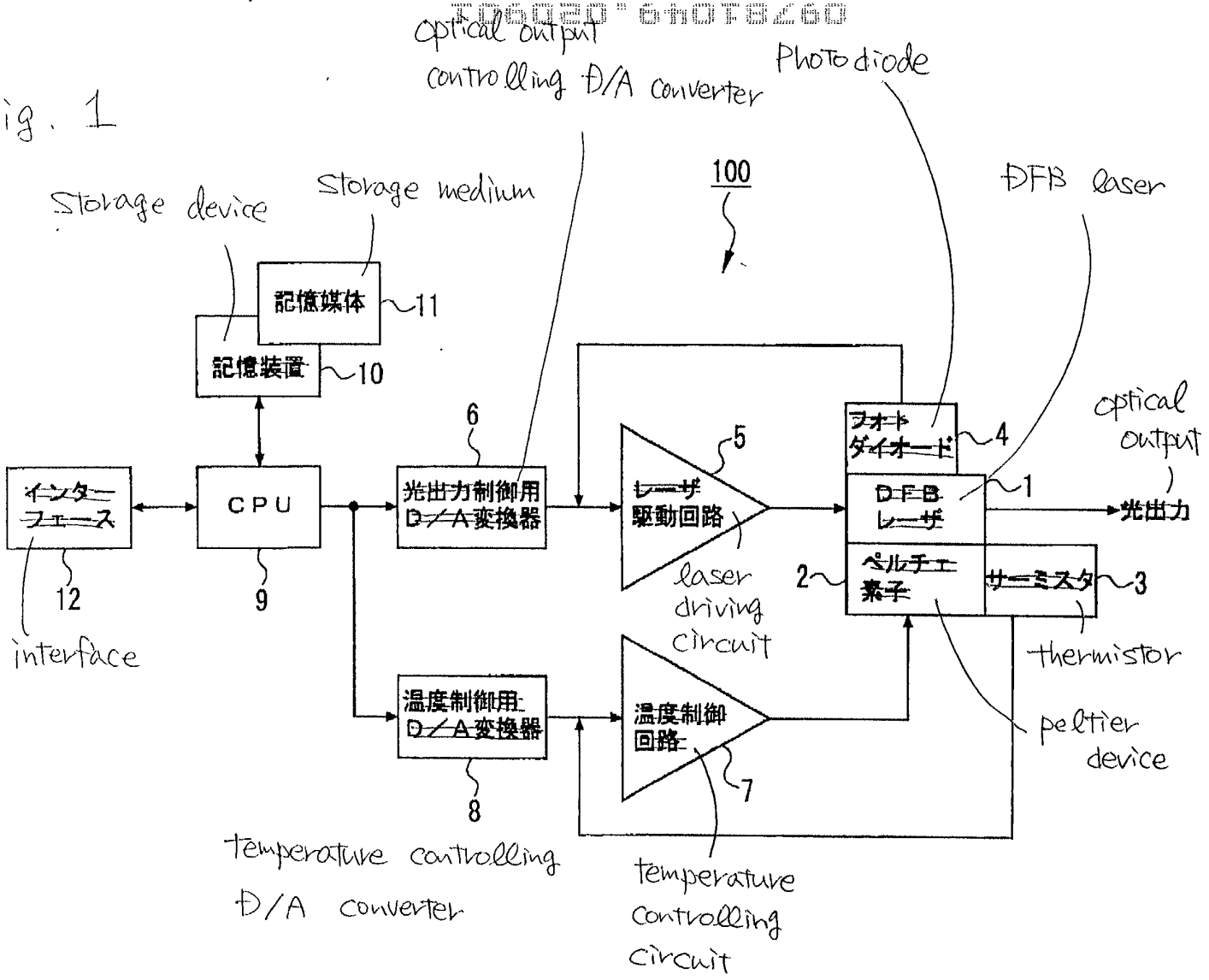
optical output controlling D/A converter
Photo diode
100
DFB laser
Storage device
Storage medium
記憶媒体
11
記憶装置
10
6
5
フォトダイオード
4
optical output
インターフェース
CPU
光出力制御用D/A変換器
レーザ駆動回路
DFBレーザ
1
光出力
12
9
laser driving circuit
2
ペルチェ素子
サーミスタ
3
interface
thermistor
温度制御用D/A変換器
温度制御回路
peltier device
8
7
temperature controlling D/A converter
temperature controlling circuit

Fig. 2

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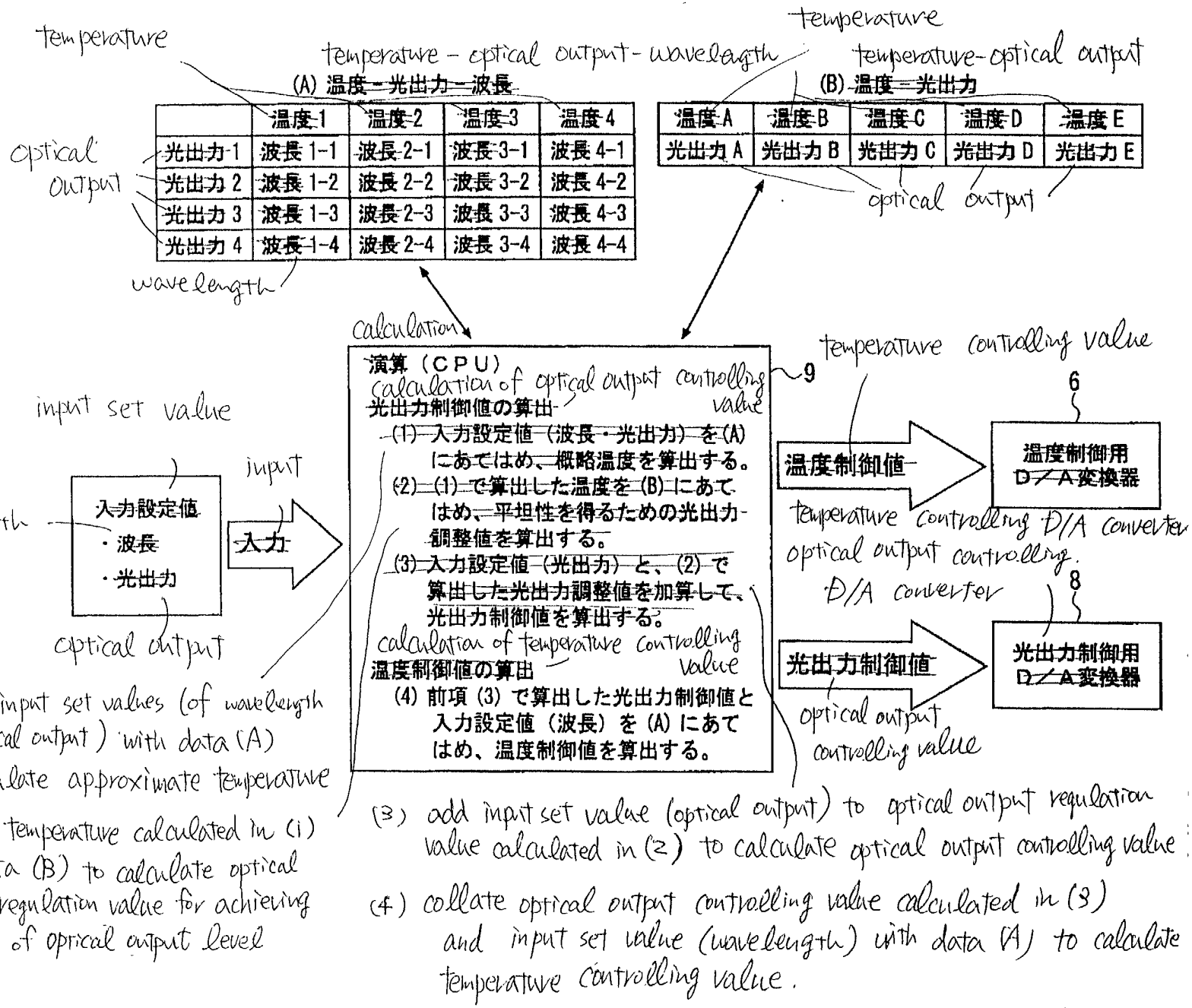

(1) collate input set values (of wavelength and optical output) with data (A) to calculate approximate temperature

(2) collate temperature calculated in (1) with data (B) to calculate optical output regulation value for achieving flatness of optical output level

(3) add input set value (optical output) to optical output regulation value calculated in (2) to calculate optical output controlling value

(4) collate optical output controlling value calculated in (3) and input set value (wavelength) with data (A) to calculate temperature controlling value.

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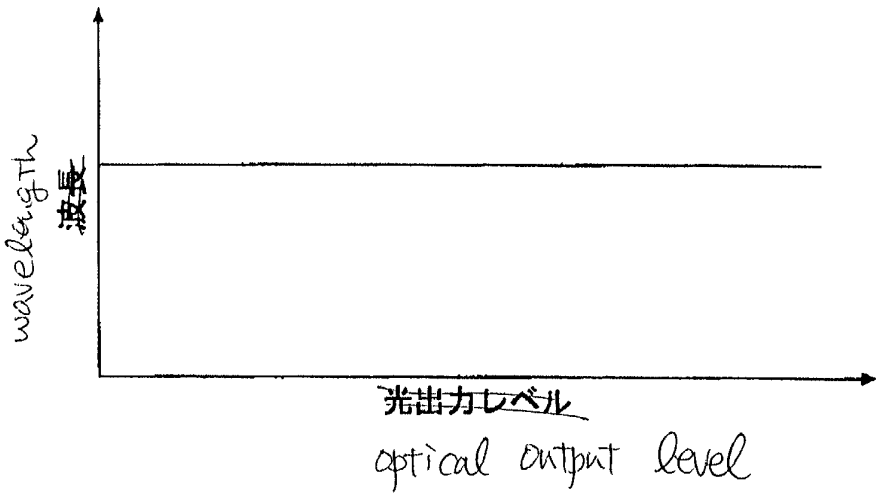
Fig. 3



JP2006120760A

Fig. 4

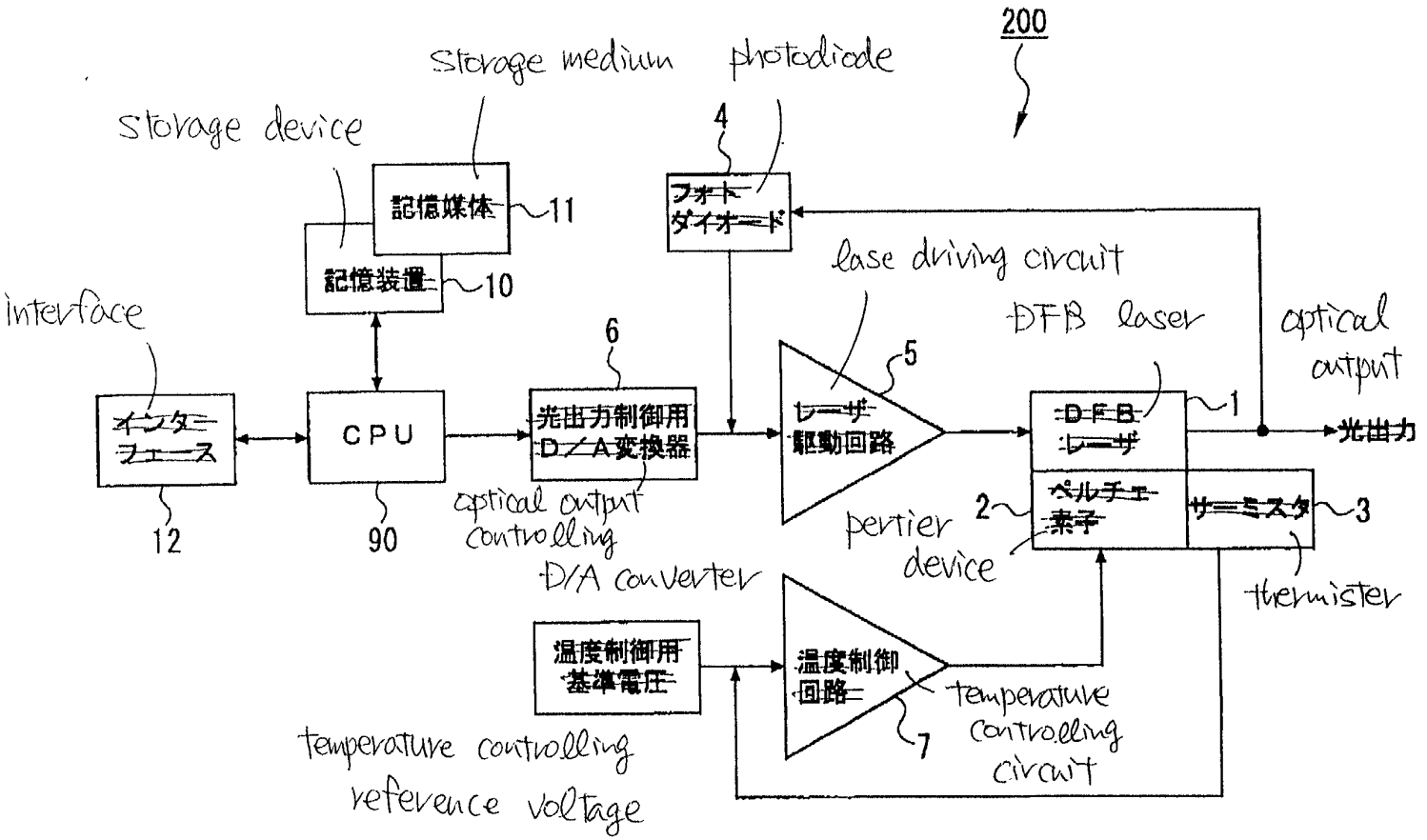


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Fig. 5A

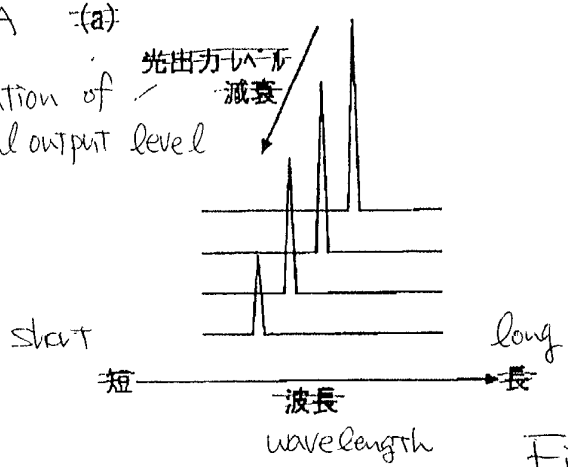

Fig. 5C

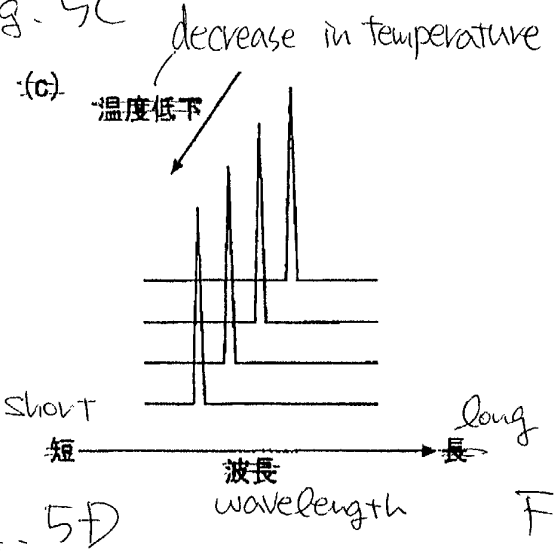

Fig. 5B

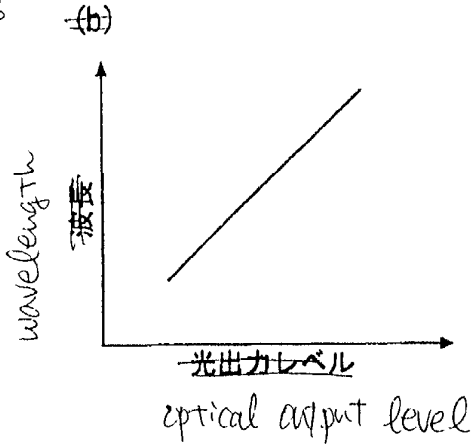

Fig. 5D

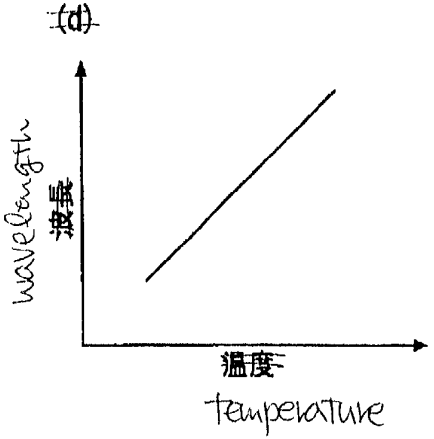

Fig. 5E

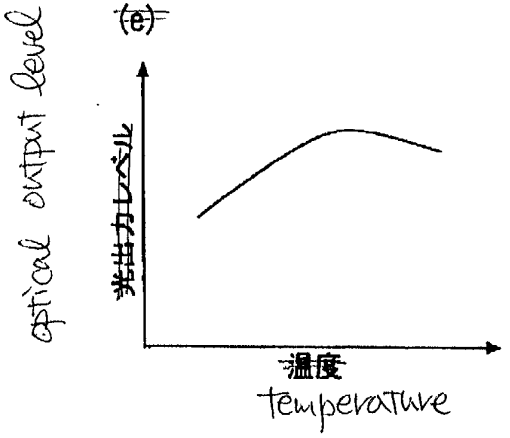
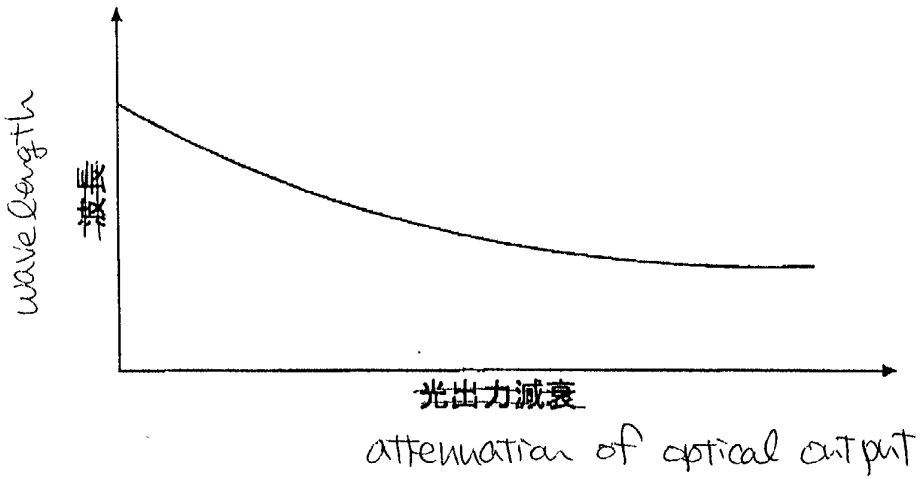
Fig. 6